Data Sheet, Doc. No. 5SYA 1458-02 Dez 18

5SNG 0450X330300
LinPak phase leg IGBT module

V_{CE} = 3300 V
I_C = 2 x 450 A

Ultra low inductance phase-leg module
Compact design with very high current density
Paralleling without derating
AlSiC base-plate for high power cycling capability
AlN substrate for low thermal resistance
Low-loss, fast and rugged SPT+ chip-set

Maximum rated values [1)]

Parameter	Symbol	Conditions	min	max	Unit
Collector-emitter voltage	V_{CES}	V_{GE} = 0 V, $T_{vj} \geq 25$ °C		3300	V
DC collector current	I_C	T_C = 105 °C, T_{vj} = 150 °C		450	A
Peak collector current	I_{CM}	t_p = 1 ms		900	A
Gate-emitter voltage	V_{GES}		-20	20	V
Total power dissipation	P_{tot}	T_C = 25 °C, T_{vj} = 150 °C		4000	W
DC forward current	I_F			450	A
Peak forward current	I_{FRM}	t_p = 1 ms		900	A
Surge current	I_{FSM}	V_R = 0 V, T_{vj} = 150 °C, t_p = 10 ms, half-sinewave		4000	A
IGBT short circuit SOA	t_{psc}	V_{CC} = 2500 V, $V_{CEM\ CHIP} \leq$ 3300 V $V_{GE} \leq$ 15 V, $T_{vj\ start} \leq$ 150 °C		10	μs
Isolation voltage	V_{isol}	1 min, f = 50 Hz		6000	V
Junction temperature	T_{vj}			175	°C
Junction operating temperature	$T_{vj(op)}$		-40	150	°C
Case temperature	T_C		-40	150	°C
Storage temperature	T_{stg}		-40	125	°C
Mounting torques	M_s	Base-heatsink, M6 screws	4	6	Nm
	M_{t1}	Main terminals, M8 screws	8	10	
	M_{t2}	Auxiliary terminals, M3 screws	0.9	1.1	

[1)] Maximum rated values indicate limits beyond which damage to the device may occur per IEC 60747

IGBT characteristic values [3)]

Parameter	Symbol	Conditions		min	typ	max	Unit
Collector (-emitter) breakdown voltage	$V_{(BR)CES}$	V_{GE} = 0 V, I_C = 10 mA, T_{vj} = 25 °C		3300			V
Collector-emitter [4)] saturation voltage	$V_{CE\ sat}$	I_C = 450 A, V_{GE} = 15 V	T_{vj} = 25 °C		2.5	2.9	V
			T_{vj} = 125 °C		3.1	3.4	V
			T_{vj} = 150 °C		3.25		V
Collector cut-off current	I_{CES}	V_{CE} = 3300 V, V_{GE} = 0 V	T_{vj} = 25 °C		0.005		mA
			T_{vj} = 125 °C		4		mA
			T_{vj} = 150 °C		15		mA
Gate leakage current	I_{GES}	V_{CE} = 0 V, V_{GE} = ± 20 V, T_{vj} = 125 °C		-500		500	nA
Gate-emitter threshold voltage	$V_{GE(TO)}$	I_C = 40 mA, V_{CE} = V_{GE}, T_{vj} = 25 °C		4.7		6.7	V
Gate charge	Q_{ge}	I_C = 450 A, V_{CE} = 1800 V, V_{GE} = -15 V ..15 V			3.3		μC
Input capacitance	C_{ies}	V_{CE} = 25 V, V_{GE} = 0 V, f = 1 MHz, T_{vj} = 25 °C			54		nF
Output capacitance	C_{oes}				3.1		nF
Reverse transfer capacitance	C_{res}				2.4		nF
Internal gate resistance	R_{Gint}	per switch			1.19		Ω
Turn-on delay time	$t_{d(on)}$	V_{CC} = 1800 V, I_C = 450 A, R_G = 1.5 Ω, C_{GE} = 0 nF, V_{GE} = ±15 V, L_σ = 30 nH, inductive load	T_{vj} = 25 °C		320		ns
			T_{vj} = 125 °C		350		ns
			T_{vj} = 150 °C		355		ns
Rise time	t_r		T_{vj} = 25 °C		75		ns
			T_{vj} = 125 °C		85		ns
			T_{vj} = 150 °C		90		ns
Turn-off delay time	$t_{d(off)}$	V_{CC} = 1800 V, I_C = 450 A, R_G = 1.5 Ω, C_{GE} = 0 nF, V_{GE} = ±15 V, L_σ = 30 nH, inductive load	T_{vj} = 25 °C		860		ns
			T_{vj} = 125 °C		1015		ns
			T_{vj} = 150 °C		1050		ns
Fall time	t_f		T_{vj} = 25 °C		250		ns
			T_{vj} = 125 °C		350		ns
			T_{vj} = 150 °C		375		ns
Turn-on switching energy	E_{on}	V_{CC} = 1800 V, I_C = 450 A, R_G = 1.5 Ω, C_{GE} = 0 nF, V_{GE} = ±15 V, L_σ = 30 nH, inductive load	T_{vj} = 25 °C		520		mJ
			T_{vj} = 125 °C		700		mJ
			T_{vj} = 150 °C		770		mJ
Turn-off switching energy	E_{off}	V_{CC} = 1800 V, I_C = 450 A, R_G = 1.5 Ω, C_{GE} = 0 nF, V_{GE} = ±15 V, L_σ = 30 nH, inductive load	T_{vj} = 25 °C		530		mJ
			T_{vj} = 125 °C		730		mJ
			T_{vj} = 150 °C		800		mJ
Short circuit current	I_{SC}	V_{CC} = 2500 V, V_{GE} = 15 V	$T_{vj\ start}$ = 150 °C		1900		A

[3)] Characteristic values according to IEC 60747 - 9
[4)] Collector-emitter saturation voltage is given at chip level

Diode characteristic values [5)]

Parameter	Symbol	Conditions		min	typ	max	Unit
Forward voltage [6)]	V_F	I_F = 450 A	T_{vj} = 25 °C		2.05	2.5	V
			T_{vj} = 125 °C		2.25	2.6	V
			T_{vj} = 150 °C		2.2		V
Peak reverse recovery current	I_{RM}	V_{CC} = 1800 V, I_F = 450 A, V_{GE} = ±15 V, R_G = 1.5 Ω, C_{GE} = 0 nF, L_σ = 30 nH, inductive load	T_{vj} = 25 °C		820		A
			T_{vj} = 125 °C		920		A
			T_{vj} = 150 °C		930		A
Recovered charge	Q_{rr}		T_{vj} = 25 °C		320		µC
			T_{vj} = 125 °C		490		µC
			T_{vj} = 150 °C		570		µC
Reverse recovery time	t_{rr}		T_{vj} = 25 °C		790		ns
			T_{vj} = 125 °C		1050		ns
			T_{vj} = 150 °C		1130		ns
Reverse recovery energy	E_{rec}		T_{vj} = 25 °C		360		mJ
			T_{vj} = 125 °C		580		mJ
			T_{vj} = 150 °C		690		mJ

[5)] Characteristic values according to IEC 60747 - 2
[6)] Forward voltage is given at chip level

NTC Thermistor

Parameter	Symbol	Conditions	min	typ	max	Unit
Rated resistor	R_{25}			4.7		kΩ
B-value	$B_{25/85}$	R2 = R25 exp [B25/85(1/T2 - 1/(298.15K))]		3371		K
	$B_{25/100}$	R2 = R25 exp [B25/100(1/T2 - 1/(298.15K))]		3435		K

Package properties [7)]

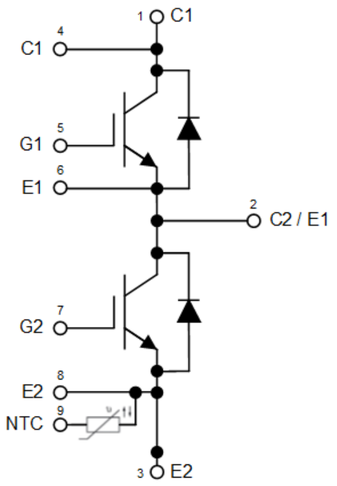
Parameter	Symbol	Conditions		min	typ	max	Unit
IGBT thermal resistance junction to case	$R_{th(j-c)IGBT}$					31	K/kW
Diode thermal resistance junction to case	$R_{th(j-c)DIODE}$					54	K/kW
IGBT thermal resistance case to heatsink	$R_{th(c-s)IGBT}$	IGBT per switch, λ grease = 1W/m x K			30		K/kW
Diode thermal resistance case to heatsink	$R_{th(c-s)DIODE}$	Diode per switch, λ grease = 1W/m x K			35		K/kW
Comparative tracking index	CTI			600			
Module stray inductance	$L_{\sigma CE}$	total C1-E2			10		nH
Resistance, terminal-chip	R_{C1E1} IGBT / Diode		T_C = 25 °C		0.25 / 0.34		mΩ
			T_C = 125 °C		0.35 / 0.47		mΩ
			T_C = 150 °C		0.37 / 0.50		mΩ
	R_{C2E2} IGBT / Diode		T_C = 25 °C		0.35 / 0.44		mΩ
			T_C = 125 °C		0.49 / 0.62		mΩ
			T_C = 150 °C		0.53 / 0.66		mΩ

Mechanical properties [7)]

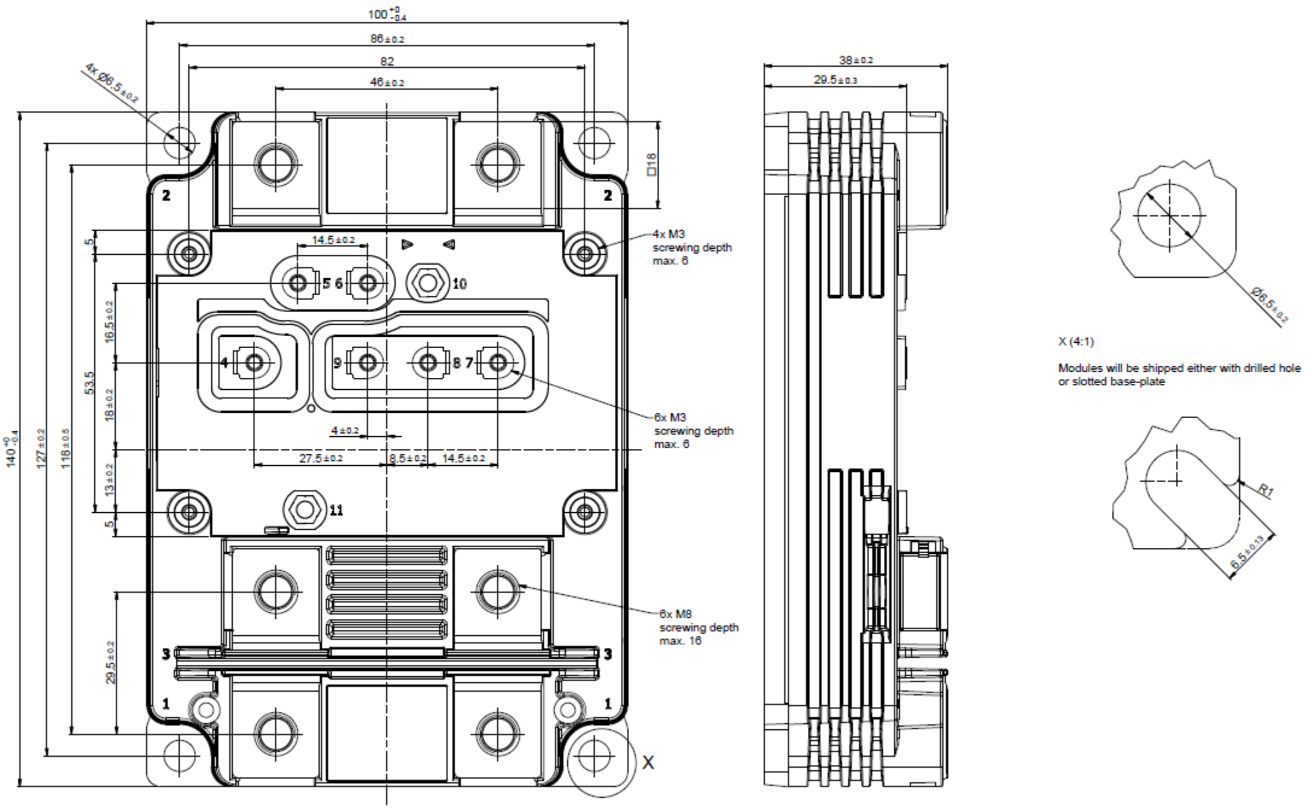
Parameter	Symbol	Conditions		min	typ	max	Unit
Dimensions	L x W x H	Typical		140 x 100 x 38			mm
Clearance distance in air	d_a	according to IEC 60664-1 and EN 50124-1	Term. to base:	20			mm
			Term. to term:	8			mm
Surface creepage distance	d_s	according to IEC 60664-1 and EN 50124-1	Term. to base:	30			mm
			Term. to term:	30			mm
Mass	m				820		g

[7)] Package and mechanical properties according to IEC 60747 - 15

Electrical configuration



Outline drawing



Note: all dimensions are shown in millimeters

This is an electrostatic sensitive device, please observe the international standard IEC 60747-1, chap. VIII.
This product has been designed and qualified for Industrial Level.

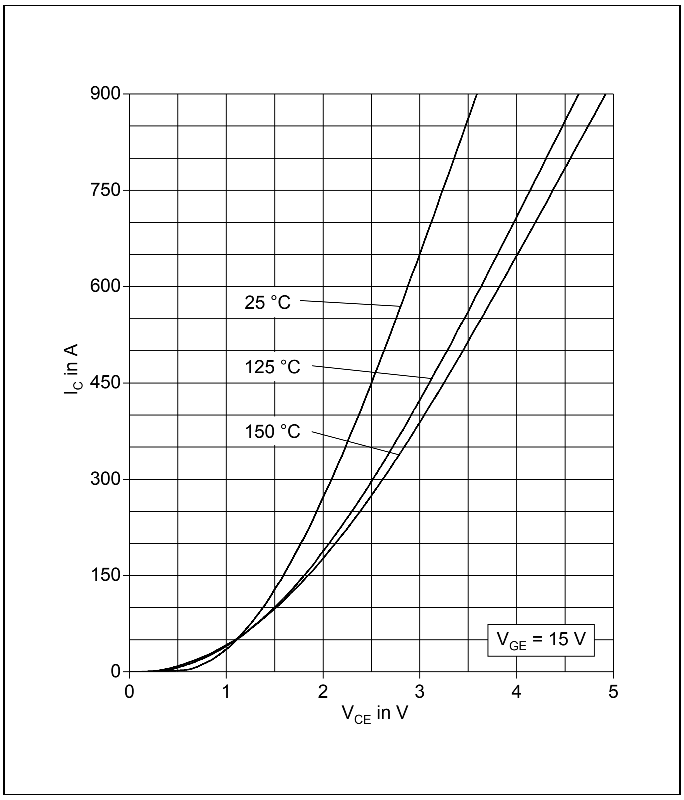


Fig. 1 Typical on-state characteristics, chip level

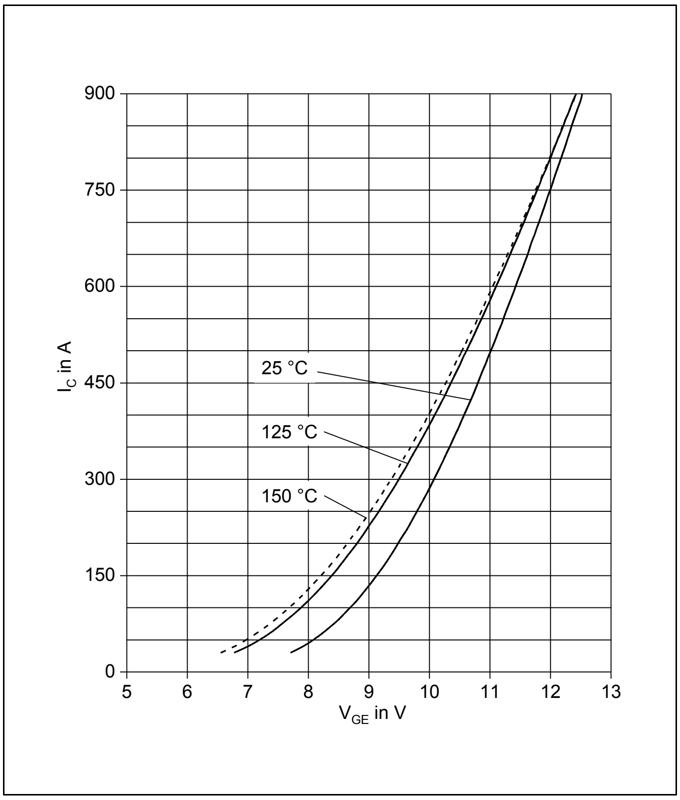


Fig. 2 Typical transfer characteristics, chip level

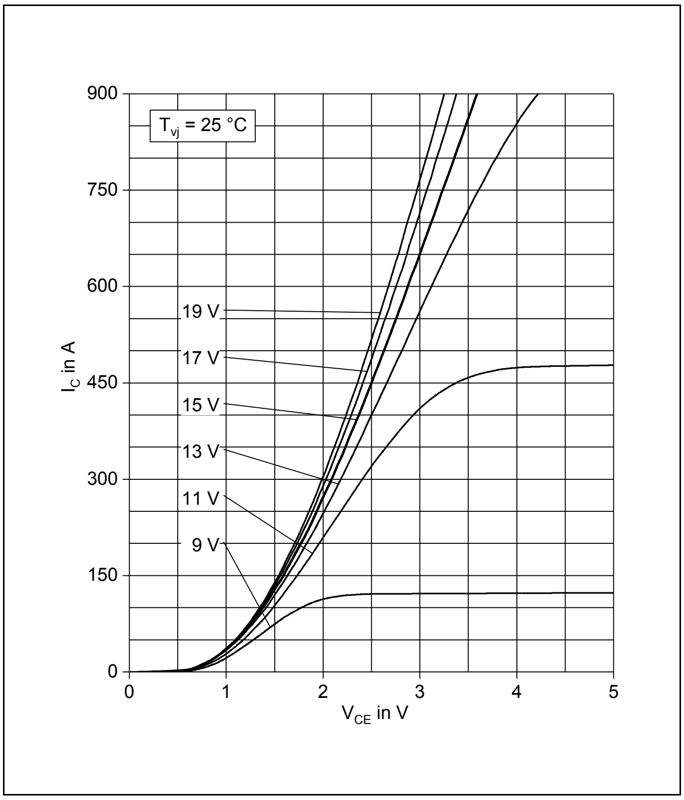


Fig. 3 Typical output characteristics, chip level

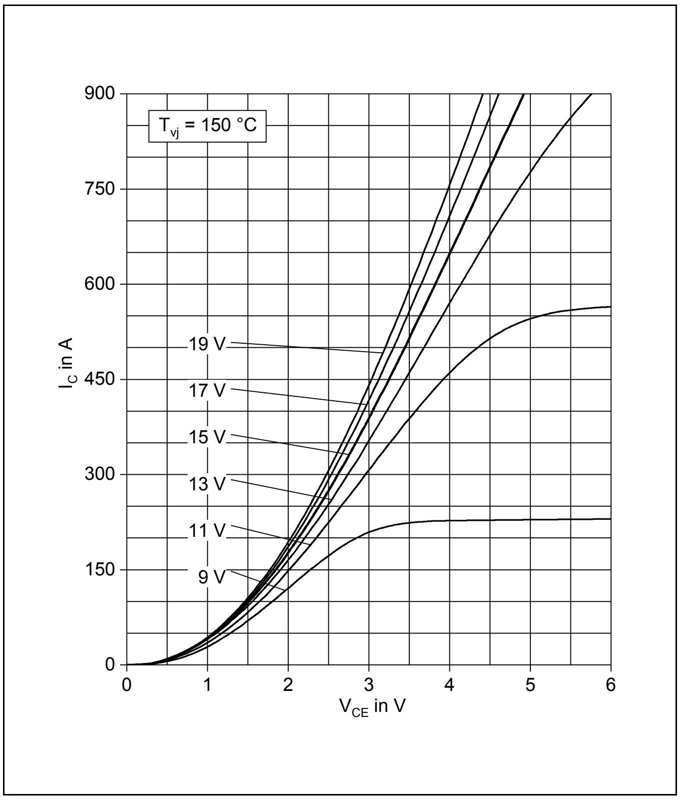


Fig. 4 Typical output characteristics, chip level

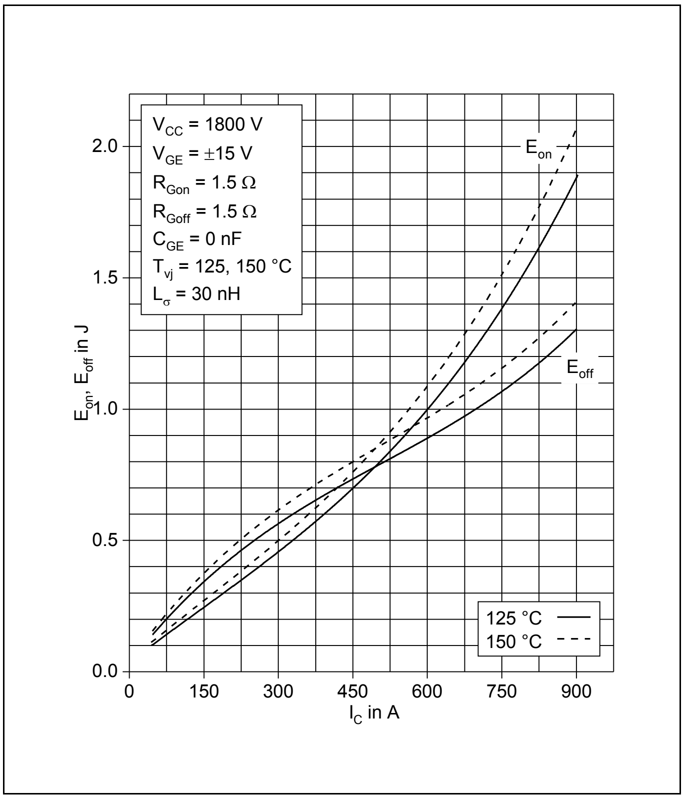


Fig. 5 Typical switching energies per pulse vs. collector current

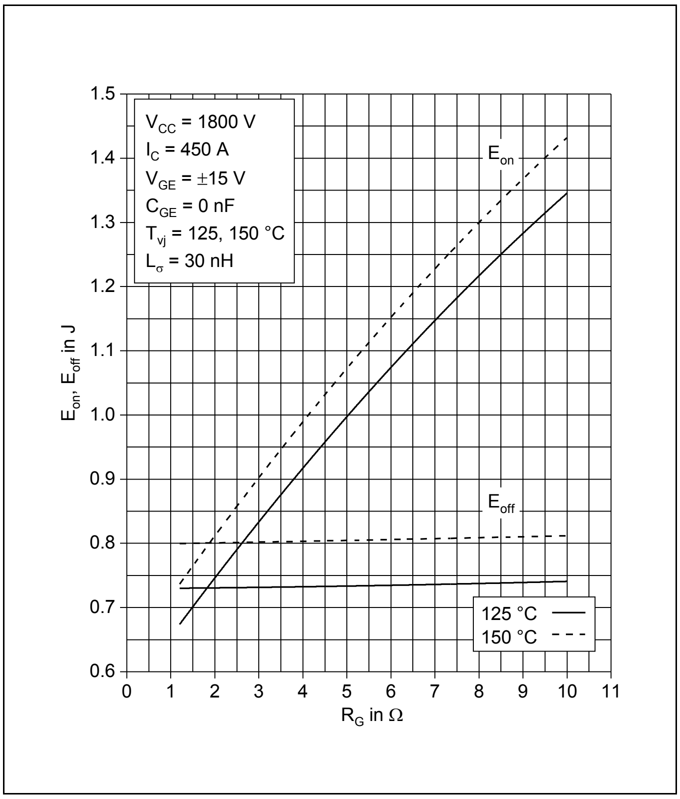


Fig. 6 Typical switching energies per pulse vs. gate resistor

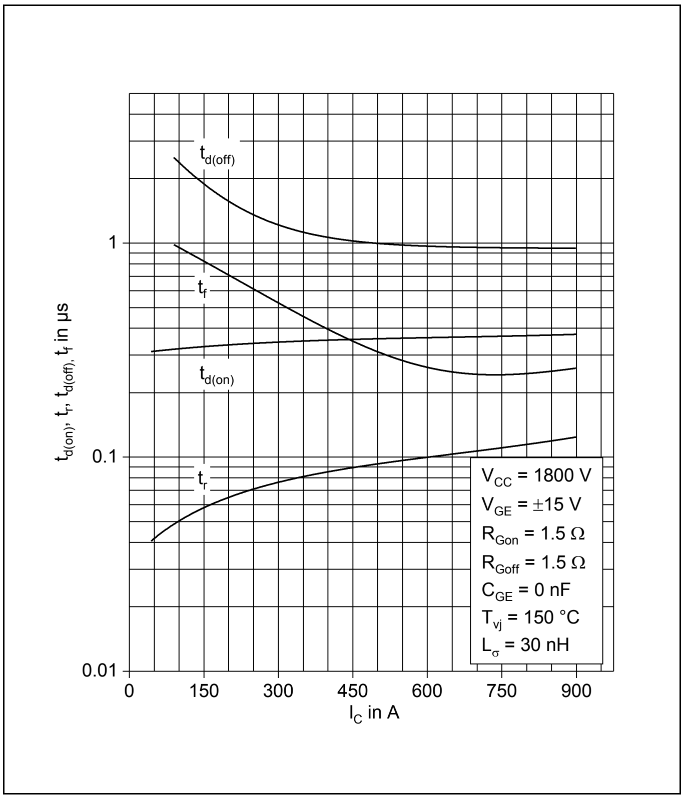


Fig. 7 Typical switching times vs. collector current

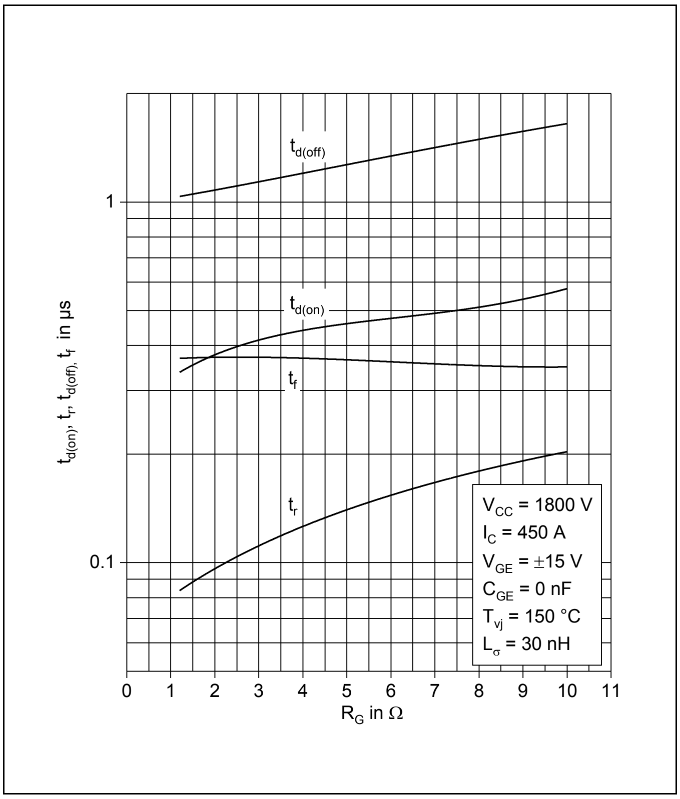


Fig. 8 Typical switching times vs. gate resistor

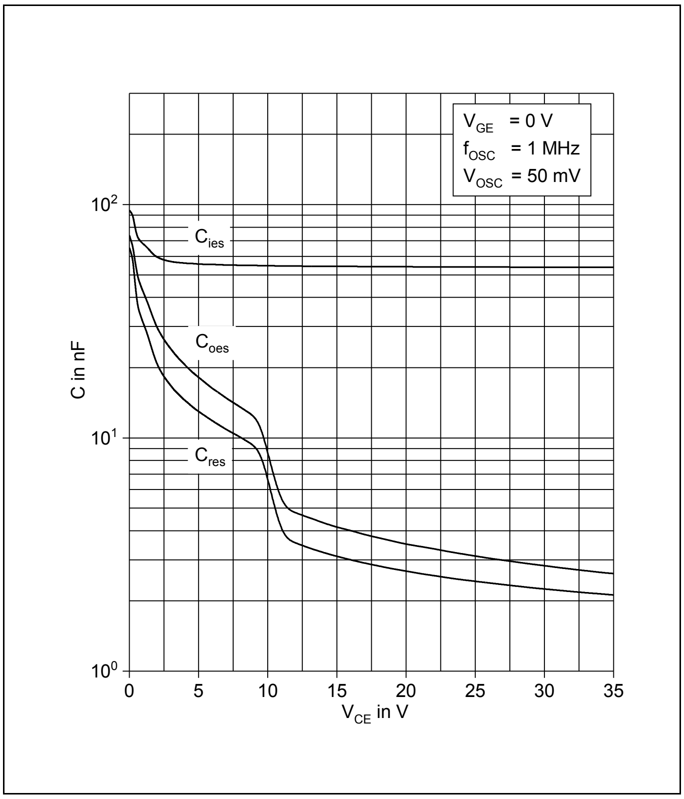


Fig. 9 Typical capacitances vs. collector-emitter voltage

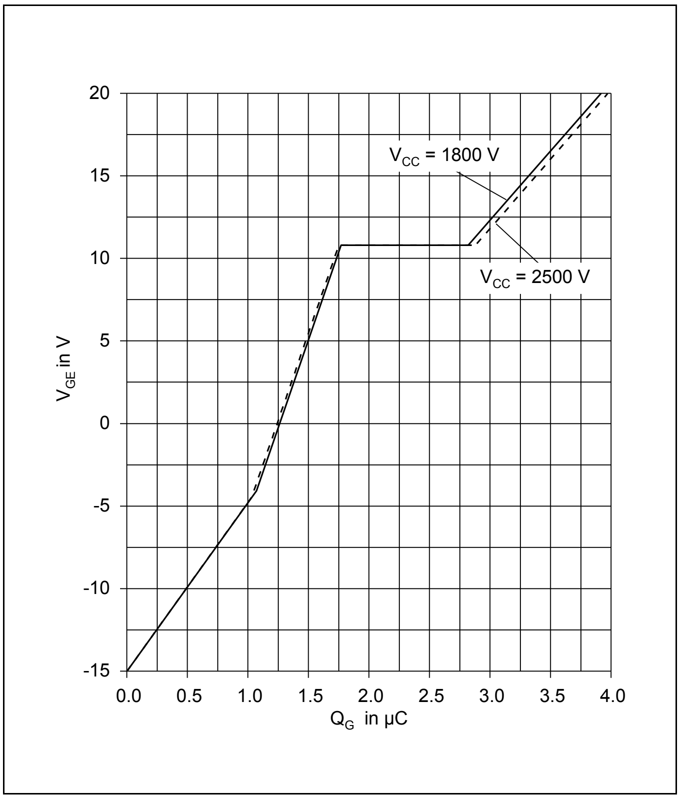


Fig. 10 Typical gate charge characteristics

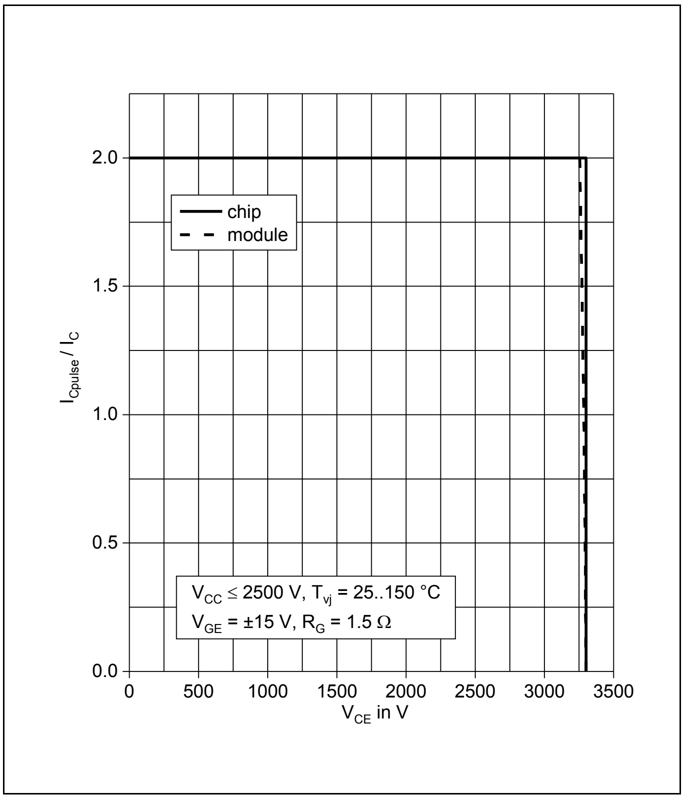


Fig. 11 Turn-off safe operating area (RBSOA)

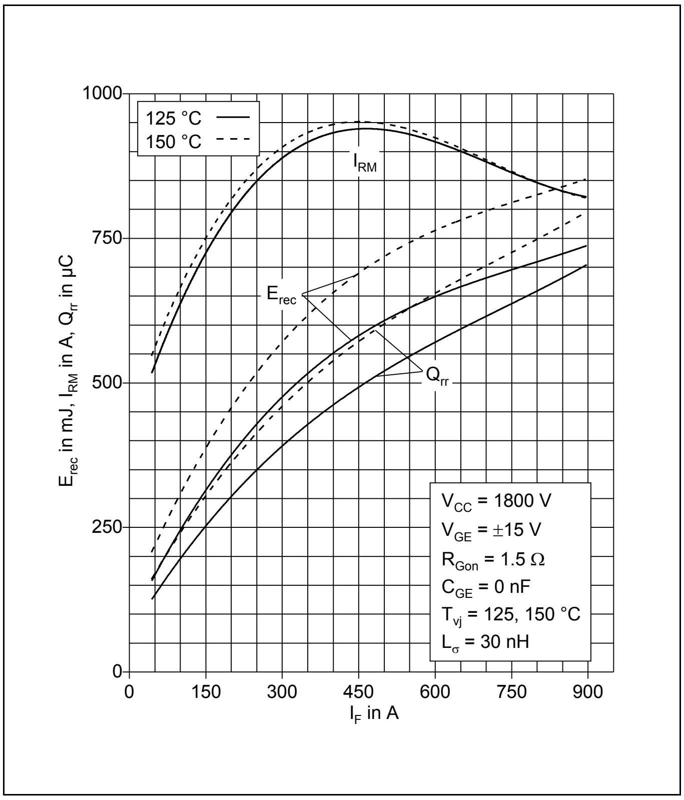


Fig. 12 Typical reverse recovery characteristics vs. forward current

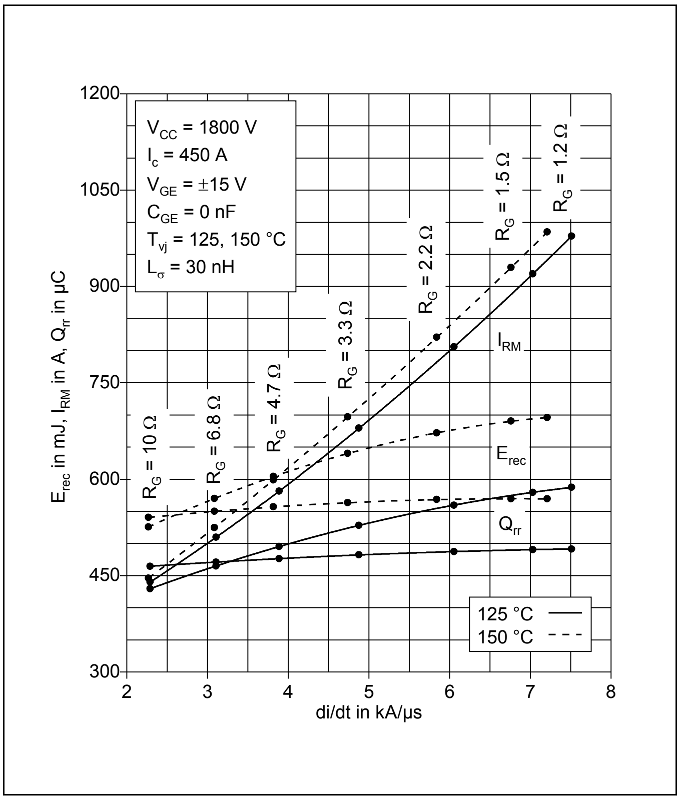


Fig. 13 Typical reverse recovery characteristics vs. di/dt

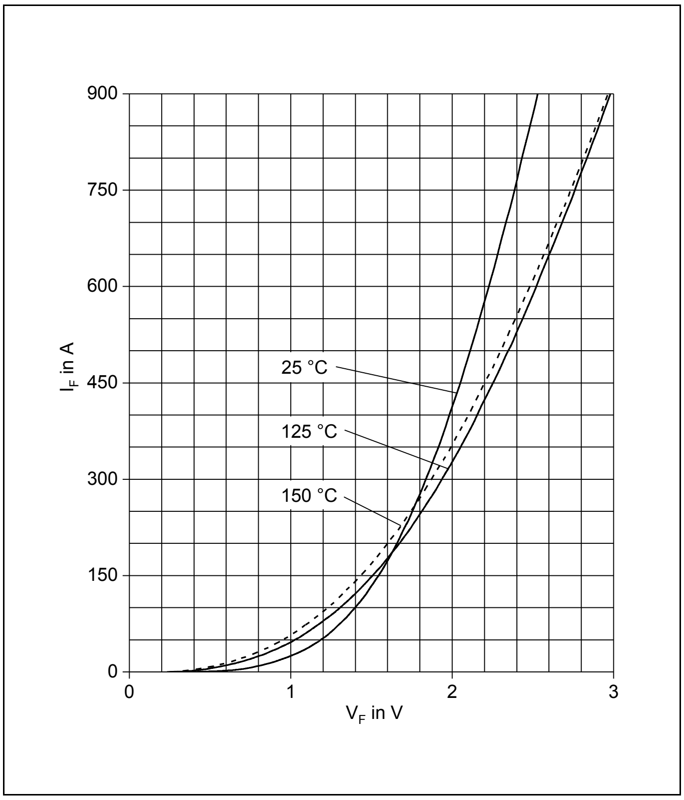


Fig. 14 Typical diode forward characteristics chip level

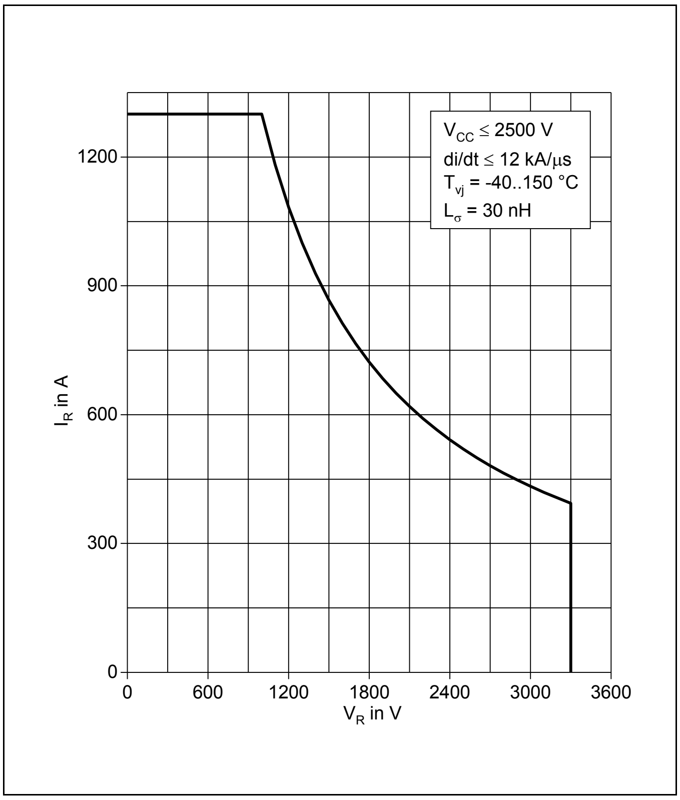


Fig. 15 Safe operating area diode (SOA)

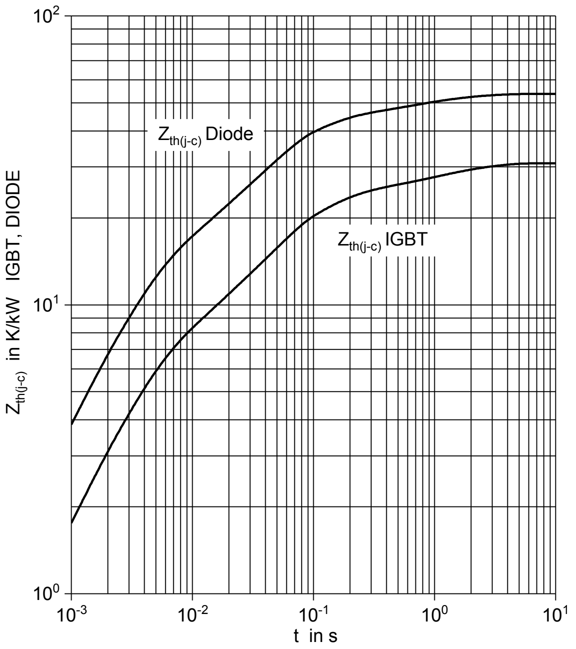


Fig. 16 Thermal impedance vs. time

Analytical function for transient thermal impedance:

$$Z_{th\,(j\text{-}c)}(t) = \sum_{i=1}^{n} R_i (1 - e^{-t/\tau_i})$$

	i	1	2	3	4	5
IGBT	Ri(K/kW)	17.38	7.31	6.23		
	τi(ms)	65.7	1220	3.67		
DIODE	Ri(K/kW)	31.1	12.6	9.95		
	τi(ms)	54.4	3.33	881		

Related documents:

5SYA 2042 Failure rates of IGBT modules due to cosmic rays
5SYA 2043 Load - cycle capability of HiPaks
5SYA 2045 Thermal runaway during blocking
5SYA 2053 Applying IGBT
5SYA 2057 IGBT diode safe operating area (SOA)
5SYA 2058 Surge currents for IGBT diodes
5SYA 2093 Thermal design of IGBT modules
5SYA 2098 Paralleling of IGBT modules
5SYA 2107 Mounting instructions for LinPak modules
5SZK 9111 Specification of environmental class for HiPak Storage
5SZK 9112 Specification of environmental class for HiPak Transportation
5SZK 9113 Specification of environmental class for HiPak Operation (Industry)
5SZK 9120 Specification of environmental class for HiPak

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